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## \*BIBDATASHEET\*

CONFIRMATION NO. 5705

Bib Data Sheet

SERIAL NUMBER 10/685,042	FILING DATE 10/14/2003  RULE	CLASS 174	GROUP ART UNIT 2841	ATTORNEY DOCKET NO. CIS03-45(7793)
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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\* NONE IB Patent

\*\* FOREIGN APPLICATIONS \*\*\*\*\* NONE IB Patent

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
 \*\* 01/15/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY CA	SHEETS DRAWING 5	TOTAL CLAIMS 24 16	INDEPENDENT CLAIMS 8 4
35 USC-119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	Verified and Acknowledged Examiner's Signature <u>[Signature]</u> Initials			

ADDRESS

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TITLE

Techniques for mounting an area array package to a circuit board using an improved pad layout.

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue )
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☐ Other

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